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24309 7590 08/02/2004

XILINX, INC
ATTN: LEGAL DEPARTMENT
2100 LOGIC DR
SAN JOSE, CA 95124

9/17/2004 EABUBAK2 00000181 240040 10787348

1 FC:1501 1330.00 DA

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Rat Slaback	(Depositor's name)
<i>Rat Slaback</i>	(Signature)
September 17, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10787,348	02/26/2004	Stephen M. Trimberger	X-1102-1P-1D US	4873

TITLE OF INVENTION: QUINTUPLE MODULAR REDUNDANCY FOR HIGH RELIABILITY CIRCUITS IMPLEMENTED IN PROGRAMMABLE LOGIC DEVICE S

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	11/02/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
TAN, VIBOL	2819	326-010000

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- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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1 Lois D. Cartier

2 _____

3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

XILINX, INC.

San Jose, California 95124

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

- ☒ Issue Fee
- ☐ Publication Fee (No small entity discount permitted)
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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is not claiming SMALL ENTITY status. See, e.g., 37 CFR 1.27(g)(2).

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(Date)

9/17/2004

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MAIL STOP ISSUE FEE
COMMISSIONER FOR PATENTS
P. O. Box 1450
Alexandria, Virginia
22313-1450

Inventor(s): Stephen M. Trimberger

Assignee: **XILINX, INC.**

Serial No.: 10/787,348

conf.no. 4873

Filed: February 26, 2004

Title: Quintuple Modular Redundancy for High Reliability Circuits Implemented in Programmable Logic Devices

Docket No.: **X-1102-1P-1D US**

Enclosed: Return Receipt Postcard
Fee Transmittal - Part B

Date: September 17, 2004

VIA FACSIMILE 703-746-4000

Atty/Sec: KAC/pbs



